D R A F T NPIC/TDS/D- -67 27 June 1967

MEMORANDUM FOR THE RECORD
SUBJECT : Chip Comparator Status Meeting
ATTENDEES:
1. On the 22nd of June the above listed individuals were given
an informad briefing on the current status of the chip comparator
problems. The significant topics discussed were:
a. The recent problems encountered and the reliaibility
and repeatibility obtained to date. The most recent problems
have all been related to the negator springs and the automatic
bias control circuits installed as the most recent modification.
is currently working on a redesigned negator
spring fix retrofit package which hopefully will eliminate the
backlash and jerking movement problems presently encountered.
Concerning the automatic bias control, the avenue of approach
is less clear, but based on the latest information, it appears
that a tolerable operation approach is available until a more
acceptable approach can be worked out, preferably by returning

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**DECLASS REVIEW by NIMA/DOD** 

at least two of the 405 B machines to

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primary problems that remain are the apparent RF interference from the drive motors and determining how long the units will remain stable without reseting the bias memory (a relatively simple operator procedure).

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b. Photo cell instability. It was pointed out to the attendees that the current visit by personnel was most likely triggered by an instability (previously unknown to in the clarex photo cells. Based on information obtained from the cell manufacturer it appears that after prolonged absence of exposure to light, the photo cells become unstable and require up to 80 hours of operation before they become stable again. After the last visit by to reinstall the interfometers significant shifts in the bias voltage were observed and it is assumed that this was a result of the photocell performance. The operational components will have to consider their operational cycle of the machines to prevent photocell instability or a retorfit will have to be made to the machine to insure that adequate light intensity hits the cells at all times. In addition to this the warm up time in general was discussed.

c. Measurement Accuracy in terms of repeatibility. It was pointed out that measurement repeatibility runs had been made recently over 4 1/2" distances with maximum deviations of

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no more than three counts (approximately .8 microns) indicating that the comparator has intrensic capability once reliability can be achieved.

- 2. It was requested that an operation procedure document be prepared to insure that the machine is properly operated and they have some feel for what maintenance criteria are required. This is particularly important in view of the sensitivity of the chip comparators.
- 3. It was also requested that the environmental requirements necessary to maintain the instrument at peak performance be set down in writing and forwarded to them at the earliest possible date so that they can take the necessary action in requesting facility modifications.

4.	In order	to minimize test	ing and checkou	ut on the	remaining	five	
machines	I intend	to take any nece	ssary action to	ship at	least two		
complete	chip comp	parators back to		so that i	the maximum	I	25X1A
amount of	debuggin	ng can be done at	the factory.				

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Approved For Release 2003/05/15; CIA-RDP78B047 memo farthe Brand Subject Chip Comparator Status meeting Attendees: 1 On the 22 of June; the above listed individuals were given an informal brifing on the current statues of the chip comparator problem. The significant topics descussed A) The vent proflems encountered and the reliability and repeatibility obtained to date The most went problems have all less related to the negator springs and the automatic bias control circuits installed on the 25X1A Approved For Release 2003/05/15/ CIA-REFT3B04747A001500030022-9

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